

SECTION III – A MARK-UP VERSION OF THE AMENDED PENDING
CLAIMS

Please amend claim 34, 40, 66 and 69 as follows:

34. (Once Amended) A storage device, comprising:

a memory buffer;

a storage medium; and

a controller circuit coupled to the memory buffer and the storage medium, the storage device and a separate storage device to couple to a host computer through a same Intelligent Drive Electronics (IDE) interface, the controller circuit to receive data through the same IDE interface and to store the data into the buffer, the controller circuit to transmit the data from the buffer to the storage medium simultaneously at least in part with the separate storage device transmitting and/or receiving data using the same IDE interface.

40. (Once Amended) A storage device, comprising:

a memory buffer;

a storage medium; and

a controller circuit coupled to the memory buffer and the storage medium, the storage device and a separate storage device to couple to a host computer through a same Intelligent Drive Electronics (IDE) interface, the controller circuit to read data from the storage medium and to store the data in the buffer simultaneously at least in part with the separate storage device transmitting and/or receiving data on the same IDE interface, the controller circuit to transmit the data from the buffer through the same IDE interface.

66. (Once Amended) A method comprising:

transmitting, from a host computer over a [signal] single Intelligent Drive Electronics (IDE) interface to which a first storage device and a second storage device are coupled, a command wherein data can be transmitted between the host computer and only one of the first storage device and the second storage device at any given time;

transmitting data between the host computer and a buffer in the second storage device over the [signal] single IDE interface responsive to the command;

transmitting data between the buffer and a storage medium in the second storage device responsive to the command; and

transmitting data between the host computer and the first storage device over the single IDE interface simultaneous with at least part of the transmitting of data between the buffer and the storage medium.

69. (Once Amended) The method of claim 66, further including:

transmitting, from the host computer over the [signal] single IDE interface, a second command to the first storage device, wherein the second command is a read or write command; and

releasing the single IDE interface for use with the first storage device only after the second storage device has completed execution of the second command.